

Amina Patel

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SUMMARY

Detail-oriented Hardware Engineer with over 5 years of experience in designing and developing high-performance electronic systems. Proven track record in improving product efficiency and reliability through innovative design solutions and rigorous testing methodologies.

EXPERIENCE

Senior Hardware Engineer

Jan 2022 - Present

Innovative Tech Solutions, San Jose, CA

- Led a team that reduced product failure rates by 30% through improved testing protocols and design iterations.
- Successfully designed and implemented a new PCB layout that decreased manufacturing costs by \$50,000 annually.
- Enhanced product performance by integrating FPGA technology, resulting in a 40% increase in processing speed.

Hardware Engineer

Jun 2018 - Dec 2021

NextGen Electronics, San Francisco, CA

- Developed and tested hardware components for consumer electronics, achieving a 20% reduction in time-to-market.
- Collaborated with cross-functional teams to implement a quality control system that improved product reliability by 25%.
- Utilized CAD tools to design circuit boards, leading to a 15% increase in production efficiency.

Junior Hardware Engineer

May 2016 - May 2018

Tech Innovations Inc., Austin, TX

- Assisted in the development of low-power embedded systems, contributing to a 10% reduction in energy consumption.
- Participated in the prototyping of new hardware products which led to successful launches, resulting in \$300,000 in revenue.
- Supported senior engineers in conducting failure analysis and implementing corrective actions.

EDUCATION

Bachelor of Science in Electrical Engineering

May 2018

University of California, Berkeley, Berkeley, CA • GPA: 3.7

SKILLS

Technical Skills: Circuit Design, FPGA Programming, Embedded Systems, Signal Processing

Tools & Frameworks: Altium Designer, MATLAB, LabVIEW